Amendments to the Claims:

This listing of claims will replace all prior versions, and listings, of claims in the application:

Listing of Claims:

1. (currently amended) A method for providing remote procedure calls in a multiprocessing system, the multiprocessing system including a general purpose processor and a plurality of network processors; each of the plurality of network processors having a memory, the method comprising the steps of:

(a) accessing a reserved address in the memory of at least one of the plurality of network processors, wherein the reserved address comprises a first portion and a second portion, wherein the reserved address is known to a remote procedure call requestor, wherein the second portion comprises a pointer for an instruction address of a procedure code, wherein the instruction address is not known to the remote procedure call requestor;

(b) initiating a software action by ~~a~~ the first portion of the reserved address, wherein the software action comprises obtaining the pointer in the second portion of the reserved address; and

(c) ~~pointing to an~~ accessing and processing the procedure code at the instruction address utilizing the pointer ~~within the memory of the at least one network processor to be processed based upon data in a second portion of the reserved address; wherein the data at the address is processed.~~

2.   (original) The method of claim 1 wherein the reserved address comprises one instruction.

3.   (original) The method of claim 1 wherein each of the network processors include a reserved address.

4.   (original) The method of claim 1 wherein a location of the reserved address of each network processor is known by the other processors.

5.   (original) The method of claim 4 wherein the reserved addresses of each network processor is in the same location of memory.

6.   (currently amended) A system for providing remote procedure calls in a multiprocessing system, the multiprocessing system including a general purpose processor and a plurality of network processors; each of the plurality of network processors having a memory, the system comprising:

means for accessing a reserved address in the memory of at least one of the plurality of network processors, wherein the reserved address comprises a first portion and a second portion, wherein the reserved address is known to a remote procedure call requestor, wherein the second portion comprises a pointer for an instruction address of a procedure code, wherein the instruction address is not known to the remote procedure call requestor;

means for initiating a software action by ~~a~~ the first portion of the reserved address, wherein the software action comprises obtaining the pointer in the second portion of the reserved address; and

means for ~~pointing to an~~ accessing and processing the procedure code at the instruction address utilizing the pointer ~~within the memory of the at least one network processor to be processed based upon data in a second portion of the reserved address; wherein the data at the address is processed~~.

7. (original) The system of claim 6 wherein the reserved address comprises one instruction.

8. (original) The system of claim 6 wherein each of the network processors include a reserved address.

9. (original) The system of claim 6 wherein a location of the reserved address of each network processor is known by the other processors.

10. (original) The system of claim 9 wherein the reserved addresses of each network processor is in the same location of memory.